



EXPRESS MAIL NO. EV207702295US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Salvatore Leonardi et al.  
Application No. : 09/747,425  
Filed : December 21, 2000  
For : RESISTIVE STRUCTURE INTEGRATED IN A  
SEMICONDUCTOR SUBSTRATE

Examiner : Junghwa M. Im  
Art Unit : 2811  
Docket No. : 856063.672  
Date : March 6, 2003

Commissioner for Patents  
Washington, DC 20231

AMENDMENT

Commissioner for Patents:

In response to the Office Action dated November 6, 2002, please extend the period of time for response one month, to expire on March 6, 2003. Enclosed are a Petition for an Extension of Time and the requisite fee.

Please amend the above-identified application as follows:

In the Specification:

Please replace the paragraph beginning at page 8, line 12, with the following rewritten paragraph:

In the winding resistive structure 81, the dielectric region 84 also surrounds the polysilicon region 83 completely, both regions conforming to the winding shape of the resistive structure 81.

In the Claims:

Please amend claims 1-4, 7, 10, and 11 and add new claims 19 and 20 as follows:

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